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"description file" and layout and "description language" and condition

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(PDF) An integrated automated layout generation system for DSP circuits

berkeley.edu (PDF)

JM Rabaey, SP Pope, RW ... - IEEE Transactions on ..., 1985 - bwr.eecs.berkeley.edu
... II. The Target Architecture This section contains only a rough description of the target architecture of our **layout** generator, as needed to elucidate the ... This FSM controls a conditional write operation by setting a **condition** code bit, de- pending upon the status of the processor. ...

Cited by 42 - Related articles - All 2 versions

VLSI CAD tool integration using the Ulysses environment

ML Bushnell, SW Director - Proceedings of the 23rd ACM/IEEE ..., 1986 - portal.acm.org
... cad-tool board file: **layout** "values" file-name "description" I CIF language **description** file for an IC ... Since the input for the **layout** synthesizer comes from several other programs, it is ... For CAD tools that automatically generate **layouts**, we want Ulysses to help the designer create ...

Cited by 42 - Related articles - All 4 versions

3D integrated circuit layout visualization using VRML

LS Indrusiak, RA da Luz Reis - Future Generation Computer Systems, 2001 - Elsevier
... The 2D **layout description** file is done using the widely used CIF format familiar to every integrated circuit **layout** designer. ... 6. Conclusions. The use of VRML as an integrated circuit **layout description** language was presented. ...

Cited by 17 - Related articles - BL Direct - All 4 versions

A novel analog module generator environment

psu.edu (PDF)

M Wolf, U Kleine, BJ Hosticka - ... of the 1996 European conference on ..., 1996 - portal.acm.org
... The design rules are stored in a technology **description** file. ... Thus a modular **layout** style results naturally. To support an automatic lay- out tool, a library with parameterizable modules is neces- sary. ... 2.1 Procedural **Layout Description** Language ...

Cited by 19 - Related articles - All 11 versions

(PDF) Computer-aided design of fuzzy systems based on generic VHDL specifications

psu.edu (PDF)

T Hollstein, SK Halgamuge, M Glesner - IEEE Transactions on Fuzzy ..., 1996 - Citeseer
... point zm equals the crisp output, since the integration is performed such, that the **condition** IX ... The decision for VHDL as **description** language has been made, since a lot of design experience with ... Physical **Layout Description** File (ASIC) or Device Configuration Bitstream (FPGA) ...

Cited by 40 - Related articles - View as HTML - BL Direct - All 8 versions

Automatic generation of FPGA routing architectures from high-level descriptions

[psu.edu](#) [PDF]

V Betz, J Rose - Proceedings of the 2000 ACM/SIGDA eighth ..., 2000 - portal.acm.org
... routed nets and path-based timing-analysis, one must specify various timing parameters in the architecture **description file**. ... ing the start points of the segments in this way makes it easier to lay out the FPGA ... A tile-based FPGA **layout** is one in which only a single logic block and its ...
[Cited by 39](#) - [Related articles](#) - [All 18 versions](#)

[CITATION] A high level synthesis tool for MOS chip design

J Dussault, CC Liaw, MM Tong - 21st Conference on Design Automation, 1984, 1984
[Cited by 21](#) - [Related articles](#) - [All 2 versions](#)

[PDF] ORTEP-3 for Windows

[gla.ac.uk](#) [PDF]

LJ Farrugia - J. Appl. Crystallogr., 1997 - chem.gla.ac.uk
... The GUI retains virtually all the useful features of Version 1, and the **layout** has remained very similar. ... read, then the style is reset to the default style Default, the contents mode is set to Use Asymmetric Unit, and a number of other options are reset to their standard **conditions**. ...
[Cited by 14486](#) - [Related articles](#) - [View as HTML](#) - [All 7 versions](#)

Bridging the paper and electronic worlds: the paper user interface

W Johnson, H Jellinek, L Klotz Jr, R Rao, ... - Proceedings of the ..., 1993 - portal.acm.org
... of digital data on paper goes beyond that of simple character and **layout** recognition, for ... a WYSIWYG form editor, a form dwixp-tion language, an action **description language**, and a ... identity of the form, locates the appropriate action **description file**, and processes the page image ...
[Cited by 119](#) - [Related articles](#) - [All 3 versions](#)

[CITATION] A vertically integrated VLSI design environment

J Rosenberg, D Boyer, J Dallen, S Daniel, C Poirier, J ... - 20th Conference on Design ..., 1983
[Cited by 9](#) - [Related articles](#) - [All 4 versions](#)

SALOGS-IV-A program to perform logic simulation and fault diagnosis

GR Case, JD Stauffer - Proceedings of the 15th Design Automation ..., 1978 - portal.acm.org
... provides for a direct preparation of input for the Sandia mask **layout** code ... The SALOGS Network **Description Language** (NDL) provides a natural way of describing circuit elements and ... The network **description file** (card deck or images) contains two parts: the model deck which is ...
[Cited by 10](#) - [Related articles](#) - [All 2 versions](#)

[PDF] A platform-independent user interface description language

[psu.edu](#) [PDF]

H Stotner - 2001 - Citeseer
... The interpretation of an UIML-Interface **description file** displays a user interface. ... structure= <part class="JFrame" name="jframe"> <style> <property name="title">Button event</property> <property name="layout">java.awt ... Page 14. Platform-Independent UI Description Language ...
[Cited by 5](#) - [Related articles](#) - [View as HTML](#) - [All 6 versions](#)

GEMS: an automatic layout tool for MIMOLA schematics

VV Venkataraman, CD Wilcox - Proceedings of the 23rd ACM/IEEE ..., 1986 - portal.acm.org
GEMS: AN AUTOMATIC LAYOUT TOOL FOR MIMOLA SCHEMATICS ... for placement of schematics
in an esthetically pleasing form are different from the constraints for placement aimed toward
minimum area **layouts**. ... The structure **description file** is analyzed for syntax errors. ...
[Cited by 7](#) - [Related articles](#) - [All 4 versions](#)

[CITATION] A new analytical approach to estimate the effects of SEUs in TMR architectures implemented through SRAM-based FPGAs

[psu.edu](#) [PDF]

L Sterpone, M Violante - IEEE Transactions on Nuclear Science, 2005
[Cited by 30](#) - [Related articles](#) - [BL Direct](#) - [All 8 versions](#)

[Test case management tools for accessibility testing](#)

[aegean.gr](#) [PDF]

S Herramhof, H Petrie, C Strobbe, E ... - ... Helping People with ..., 2006 - Springer
... The Test Case **Description Language** TC DL [2] developed within the scope of BenToWeb, not
only states whether a test ... 1. Status information about a test case **description file** ... Open questions
need information about **layout** and spacing to be presented correctly to user panel by ...
[Cited by 8](#) - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)

[Multithreaded, mixed hardware description languages logic simulation on engineering workstations](#)

T Chan - US Patent 6,466,898, 2002 - Google Patents
Page 1. US006466898B1 (12) United States Patent Chan (to) Patent No.: US
6,466,898 B1 (45) Date of Patent: Oct. 15, 2002 (54) MULTITHREADED, MIXED
HARDWARE DESCRIPTION LANGUAGES LOGIC SIMULATION ON ...
[Cited by 18](#) - [Related articles](#)

[CITATION] Arden-architecture development environment

[ntu.edu.tw](#) [PDF]

F Lai, F Tsaor, RJ Shang - ..., Communications and Automation towards the 21st ..., 1992
[Cited by 5](#) - [Related articles](#) - [All 4 versions](#)

[Mapping physical formats to logical models to extract data and metadata: The defuddle parsing engine](#)

[pnl.gov](#) [PDF]

T Talbott, K Schuchardt, E Stephan, J Myers - ... and Annotation of Data, 2006 - Springer
... parser supports the description of the content, structure and physical **layout** (endian-ness ...
Applications can utilize the ESMIL library to parse this **description file** and transparently ... A recent
development in descriptive parsers is the Data Format **Description Language** (DFDL) [13] ...
[Cited by 6](#) - [Related articles](#) - [BL Direct](#) - [All 7 versions](#)

[An object-oriented descriptive language to facilitate advanced handwritten form processing](#)

C Cracknell, AC Downton, L Du - Image and Vision Computing, 1998 - Elsevier
... Other stages will also benefit from the encoding of extra information within a form **description**
file. ... The Form **Description Language** (FDL) proposed by Tang et al. [10] is primarily concerned
with allowing the **layout** of a form design's field objects to be recorded. Liu et al. ...
[Cited by 4](#) - [Related articles](#) - [All 5 versions](#)

[CITATION] An overview of the COBRA-ABS high level synthesis system for multi-FPGA systems

AA Duncan, DC Hendry, P Gray - IEEE Symposium on FPGAs for Custom 1998

Cited by 30 - Related articles - All 4 versions

Recent Advances in the CLiDE Project: Logical Layout Analysis of Chemical Documents

A Simon, AP Johnson - J. Chem. Inf. Comput. Sci. 1997 - ACS Publications

... They are important for differentiating between **layouts** occurring under the described page conditions, ie, to ... the output is entirely correct according to the specification given in the style description file. ... left) A sample image and (b. right) the results of logical **layout** analysis (Source ...

Cited by 2 - Related articles - BL Direct - All 4 versions

Imposition in a raster image processor

CH Benson, JT Green, M Parker - US Patent 6,411,396, 2002 - Google Patents

... A page **description language** file sent to an imagesetter specifies objects to be displayed and 55 related ... on the availability of a job ticket and the receipt of the page **description** file page(s) ... The job ticket includes **layout** information including a list of sheets (surfaces) to be printed ...

Cited by 4 - Related articles - All 2 versions

A unified header compression framework for low-bandwidth links

J Lillev, J Yang, H Balakrishnan, S ... - Proceedings of the 6th 2000 - portal.acm.org

... Figure 2: **Layout** of a typical compressed header, including Context ID, bit-field for the ... Since the header **description language** was described in Section 3, the latter two parts occupy this ... that the tools use to translate the header information found in the **description file** to protocol ...

Cited by 29 - Related articles - All 30 versions

psu.edu [PDF]

Microcode development for microprogrammed processors

J Hwang, CA Papachristou, DD ... - Proceedings of the 18th 1985 - portal.acm.org

... Most notable examples of such **layout** languages are the Mead and Conway CIF [18], LAVA [19], LGEN [20], CHISEL [21] ... 2. THE **HARDWARE DESCRIPTION LANGUAGE** MDSL ... 1. The statement `b*1000: A0 <- A+T` means that under the control **condition** binary 1000 for the ALU ...

Cited by 3 - Related articles - All 3 versions

Automatic calibration for output devices based on device output mode

H Van Hoof, P Adriaensen - US Patent 6.067.406, 2000 - Google Patents

... This linearization is also a necessary condition for the proper behaviour of the colour ... if necessary and before applying the halftone the output device change between layout completion and 35 ...

Moreover, different 40 For a page description language such as PostScript, a types of ...

Cited by 6 - Related articles

Automation systems for generating web-based mathematical learning and presentation materials

Y Shiota - International Journal of Computational Science and ..., 2006 - Inderscience

... **layout** representations are written in HTML in which the logical structures and physical **layouts** are not ... the maximum profit of a firm (Figure 5). In the meta-level **description file** shown in ... is that they produce web pages of uniform appearance whose content, **layout**, and generated ...

Cited by 3 - Related articles - BL Direct - All 6 versions

Tools for remote computing in accelerator control

[cern.ch](#) [PDF]

PS Anderssen, V Frammery Rainer - Nuclear Instruments and Methods in ..., 1990 - Elsevier
... To write, the Interface **Description File**, a special **description language** is used. it is based on the MESA language from Xerox [13] and its grammar is fully specified in ref. [11], using the PachusNaur format. The **layout** of the file is shown in fig. ...

[Cited by 3](#) - [Related articles](#) - [All 4 versions](#)

[PDF] Description mechanisms supporting the configuration and management of TTP/A fieldbus systems

[tuwien.ac.at](#) [PDF]

S Pitsek - Master's thesis, Technische Universität Wien, Institut ..., 2002 - vmars.tuwien.ac.at
... 36 iii Page 5. 3.1.3 Standard Generalized Markup Language (SGML) ... 36 3.1.4
Extensible Markup Language (XML) ... 37 3.1.5 Device **Description Language** (DDL) ...
39 3.2 Configuration and Management Frameworks ... 40 ...

[Cited by 9](#) - [Related articles](#) - [View as HTML](#)

Parallel redundant interpretation in a raster image processor

MR Hughes - US Patent 6,295,134, 2001 - Google Patents
... defined a page **description file**, the method including receiving a page **description file** defining one or ... 16 is configured to receive page **description language** input data files and **layout** information ... A page **description language** file sent to an digital press specifies objects to be ...

[Cited by 2](#) - [Related articles](#)

Design of Automation Systems for Web Based Courseware Using XML and XSLT

Y Shiota - Databases in Networked Information Systems, 2005 - Springer
... Meanwhile, their disadvantage is that they produce Web pages of uniform appearance whose content, **layout** and generated dialogues can thus be tedious for the viewer. ... (5) Separate the various parts of the **condition**. ... Definition Data of "Problem A" (Meta-level **Description File**) ...

[Cited by 2](#) - [Related articles](#) - [All 3 versions](#)

Method and system for maintaining hierarchy throughout the integrated circuit design process

C Carruthers, I Buchanan - US Patent 6,370,677, 2002 - Google Patents
... Similarly, Hardware **Description Language** (HDL) tools incorporate hierarchy in a manner similar to a software ... Thus, specific nodes may have a special custom **layout** to satisfy a particular ... present invention is the ability to access multiple physical cell **layouts** without disturbing ...

[Cited by 8](#) - [Related articles](#) - [All 2 versions](#)

FACE: Fine-tuned Architecture Codesign Environment for ASIP Development

[ntu.edu.tw](#) [PDF]

IH Jeng, F Lai, YD Tseng - Design Automation for Embedded Systems, 1999 - Springer
... user and machine **description file** contains constant definitions of register, storage, and stack **layout**. ... Architecture **description file** shares information in-between to make partition ... source code, where as in others software parts are translated into a hardware **description language**. ...

[Cited by 2](#) - [Related articles](#) - [All 3 versions](#)

[CITATION] Logal: a CHDL for Logic Design and Synthesis of Computers

JH Stewart - Computer, 1977

[Cited by 1](#) - [Related articles](#) - [All 4 versions](#)

A MODEL-based object-oriented topology specification for network management

[fcu.edu.tw](#) [PDF]

JY Chen, DL Yang, AC Liu - 2006 - [dspace.lib.fcu.edu.tw](#)

... ROC IP discovery and **layout** processes ... a better way for users to configure their network topology, and automatically generate a corresponding MODEL **description file**. ... [12] ITU-T Recommendation Z.100, "CCITT Specification and **Description Language (SDL)**," Recommendation ...

[Cited by 5](#) - [Related articles](#) - [All 5 versions](#)

[PDF] C Compiler Aided Design of Application-Specific Instruction-Set Processors Using the Machine Description Language [psu.edu](#) [PDF]

LISA

O Wahlen, UDIS Heinen - D thesis submitted to Aachen University of ..., 2004 - CiteSeer
... D 82 (Diss. RWTH Aachen) Shaker Verlag Aachen 2004 Berichte aus der Elektrotechnik Oliver
Wahlen C Compiler Aided Design of Application-Specific Instruction-Set Processors Using the
Machine **Description Language** LISA . Page 4. ... 109 A.2 Data **Layout** Dialog

[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 7 versions](#)

Verilog to vital translator

N Fakhry, V Lakshmanan - US Patent 6,668,359, 2003 - Google Patents

... EMBODIMENTS The Very High Speed Integrated Circuit (VHSIC) Hard- ware **Description Language (VHDL)** Initiative ... easier task to automatically drive the synthesis and physical **layout** tools without ... map file, a gate primitives map file, a VERILOG **description file**, a standard ...

[Cited by 1](#) - [Related articles](#)

File arrangement and maintenance executive (FAME). A general purpose schema driven file maintenance system

BW Brown, J McConathy, M Meigs, B Munger, ... - Computers in Biology ..., 1978 - Elsevier
... 4. Specification of the **layout** of the first card (Fig. 1) of the Header file for entry into FAME. ... The next **condition** is embedded blanks which may indicate a card which is mispunched by several columns. ... 2. CODASYL Data **Description Language** Journal of Development. ...

[Cited by 4](#) - [Related articles](#) - [All 2 versions](#)

Method and apparatus for calibrating halftone output from an imagesetting device

JM Burns - US Patent 5,170,257, 1992 - Google Patents

... OTHER PUBLICATIONS Listing of an "Aldus Printer **Description File**" (Jun. ... First, an application pro- grammed (a drawing or page **layout** package) is used to find what is wanted or actually printed on the paper or film. ... Page **description language** individual output sense **conditions**. ...

[Cited by 6](#) - [Related articles](#)

[PDF] Substation Automation based on IEC 61850

[abb.com](#) [PDF]

I De Mesmaeker, P Rietmann, KP Brand, P ... - 6th Regional CIGRÉ ..., 2005 - abb.com
... The use of the already mentioned standardized Substation Configuration **description Language (SCL)** has an ... Also the geographical **layout** (extension, buildings, cable channels, etc) may have an impact ... documented also with a SCL file called IED Capability **Description file (ICD)** ...

[Cited by 3](#) - [Related articles](#) - [View as HTML](#) - [All 3 versions](#)

User interface design assistance for large-scale software development

[usc.edu \(PDF\)](#)

GA Bolcer - Automated Software Engineering, 1995 - Springer

... Jade[20] uses a textual description to automatically **lay out** look-and-feel independent user interfaces. ... Style and **layout** knowledge-base rules are separated into families. ... Each project has a project **description file** ("projectname.P"), a list of interface description files ("filename.G ...

[Cited by 2](#) - [Related articles](#) - [All 11 versions](#)

(PDF) COBOL/SRE: A COBOL System Renovation Environment

[psu.edu \(PDF\)](#)

A Engberts, W Kozaczynski, E Liongosari, JQ ... - Proc. of the 6th Intl. ..., 1993 - Citeseer

... Alternatively, the user may provide a system **description file** to load and link the files into COBOL/SRE automatically in one pass ... A data **layout** graph of data record WS-TRANS- RECORD is shown in the front window in Figure 4. Data objects in the graph ... **Condition-Based Slicing** ...

[Cited by 5](#) - [Related articles](#) - [View as HTML](#) - [All 35 versions](#)

Logic synthesizer

M Komoda - US Patent 5,438,524, 1995 - Google Patents

... large wiring load is charged as well as adverse effects described in HDL (Hardware **Description Language**). ... worst case, there arises the problem that description received from the function **description file** 5 wirings ... do into a logic such as AND, OR, etc., and it further com- **layout** ...

[Cited by 6](#) - [Related articles](#)

From coarse-grained components to DVE applications: a service-and component-based framework

X Zhang, D Granin - ... of the twelfth international conference on 3D ..., 2007 - portal.acm.org

... map their design to the implementation, the framework provides a high level **description language** for specify ... The format of the behaviors **description file** is described in Section 4.3. ... describes the construction principles, communication rules, interaction logic and the **layout** of the ...

[Cited by 9](#) - [Related articles](#)

Method and system for separation of content and layout definitions in a display description

MP Crider, I Zaika, MR Labarge - US Patent App. 10/780,547, 2004 - Google Patents

... This separation allows alternate **layouts** to be specified that are conditionally applied to content of ... vertical " J 901 retrieve area for element 902 count children 903 set **layout** information return ...
2 of 9 US 2005/0183008 A1 display system 200 206 display **description file** 201 207 ...

An overview of the sysedit chip design environment

S Rust - Microprocessing and Microprogramming, 1990 - Elsevier

... These elements are translated to the hardware **description language** KARL-III for simulation. ... be specified directly RT-elements can be estimated by their parameters (ie the word- size) A **description file** gives the mapping of the design modules to predesigned **layout** elements ...

[All 4 versions](#)

A drawing editor key 3 with capabilities of defining figure parts and preserving connection/inclusion relationships

T Matsuura, H Naota, M ... - Systems and Computers ..., 1991 - interscience.wiley.com

... The following **conditions** must be satisfied in handling the connection/inclusion relationships among figures. ... The later processing is the same as in the part definition by the figure **description language**. ... Same in Tables 3 and 4. Table 3. Time for layout modification (from Figs. ...

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[\[PDF\] DEVELOPING AN XML LANGUAGE FOR CREDIBLE WIRELESS NETWORK SIMULATION](#)

[bucknell.edu](#) [PDF]

AW HALLAGAN, LF PERRONE - 2009 - bucknell.edu

... in the network (which represent individual computers), node density, coverage area, geographical layout, etc. ... of different steps in the process; the generation of the model **description file** is no ... files as input, and translates them into the correct model **description language** for SWAN ...

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[\[PDF\] An Integrated Automated Layout Generation System for DSP Circuits](#)

[berkeley.edu](#) [PDF]

A Sangiovanni-Vincentelli - IEEE TRANSACTIONS ON ..., 1985 - bwr.ecs.berkeley.edu

... II. The Target Architecture This section contains only a rough description of the target architecture of our layout generator, as needed to elucidate the ... This FSM controls a conditional write operation by setting a **condition** code bit, depending upon the status of the processor. ...

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[\[CITATION\] SCoP 4: A General-Purpose Interactive Simulation Package for Personal Computers](#)

JM Kootsey, CR Martin - ... of the SCS ..., 1991 - Society for Computer Simulation ...

[Method of generating ASIC design database](#)

A Nakamura - US Patent App. 11/352,288, 2006 - Google Patents

... The generated RTL description is also based on a hardware **description language** (HDL). ... 0009] The specification data contains data on the process time, layout area, power consumption, test cost, etc. ... 0010] The RTL description data is an RTL **description file** in units of an entity. ...

[All 2 versions](#)

[File Delivery Session Handling](#)

T Paila - US Patent App. 11/575,396, 2005 - Google Patents

... session is referred in the SMIL, at least one SDP (or other session **description**) **file** in the ... different display layouts, and define when in the presentation the different display layouts are operative. ... at which the content is rendered to vary from a given template or layout during the ...

[All 4 versions](#)

[\[CITATION\] ROBOTIZED ASSEMBLY OF GENEVA GEARS-A CASE STUDY FOR EDUCATION IN ROBOTICS](#)

G Kronreif - ... (SYSID'94): a postprint volume from ..., 1995 - Published for the International ...

[Related articles](#)

[Method and system for embedding information in document](#)

Y Hirayama, T Amano, S Shimizu, N ... - US Patent 6,782,509, 2004 - Google Patents

... watermarking of text, this technique satisfies **condition 1**, and as far as **condition 2** is ... art 4) is employed to embed electronic watermark information in a page **description language**, such as ...

sequences of 55 locations are designated based on a description of the **layout** structure of ...

[CITATION] ARSENIC: METHODOLOGY AND IMPLEMENTATION

DDGJJ Bozek - ... Logic in Computer Science: June 4 ..., 1984 - IEEE Computer Society Press

[Related articles](#)

[PDF] Automatic generation of a processor design in IDaSS from an instruction set description in SimDes

[tue.nl](#) [PDF]

BAM Rietkerken... - alexandria.tue.nl

... Chapter 4 deals with the **layout** generation tool itself. ... Initially the processor **description language**

SimDes was designed for SimGen usage [Takk92]. ... The SimDes language allows the description

of multiple implementations of a single processor in one single **description file**. ...

[Related articles](#) - [View as HTML](#) - [All 3 versions](#)

Integrated circuit design system and method using preprocessor which changes hardware description in accordance with configuration

T Tamai - US Patent App. 10/602,618, 2003 - Google Patents

... In the logic synthesis program 25, processing for converting the circuit **description file** 24 into

a circuit **description file** using a cell as a basic unit of a circuit is ... At this time, how to **lay out** cells

and wirings is not determined yet. ... [0039] The netlist 29 is processed by a **layout wiring**

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[PDF] LOGAL: A CHDL for Logic Design and Synthesis of Computers

[psu.edu](#) [PDF]

JH Stewart - Citeseer

... To optimize these conflicting requirements, the logic designer must design algorithms and **lay**

out the logic several times ... The logic design group (see Figure 2) creates the LOGAL design

description file for a unit interactively from ... LOGIC FAMILY INITIAL LAYOUT RULES LOGIC ...

[Related articles](#) - [View as HTML](#)

ADAM: an adaptive multimedia content description mechanism and its application in web-based learning

C Wang, DZ Wang, JL Lin - Expert Systems with Applications, 2010 - Elsevier

... In this subsection, we introduce an XML-based content **description language**, called Adaptive

Multimedia Markup Language ... 4, an AMML **description file** is consisted of three major blocks. ... block

describes a presentation's properties of the spatial dimension, ie, the **layout** of the ...

Integrated Information Support System (IIS). Volume 5. Common Data Model Subsystem. Part 20. Neutral Data Manipulation Language (NDML) Precompiler ...

[dtic.mil](#) [PDF]

ML Apicella, J Stalon, B Levi, CONTROL DATA CORP ... - 1990 - oai.dtic.mil

... 1. Generate the Data Division section of the Request Processor. a) Generate file

description and record **layout** if the NDML request resulted in a retrieval subtransaction.

2. Generate the Working Storage section of the Request Processor. ...

[View as HTML](#) - [All 3 versions](#)

Development of a remote monitoring system based on Grid-EPICS for tokamak experiments

IS Choi, H Jhang, KH Kim - Fusion Engineering and Design, 2008 - Elsevier

... For instance, the Tokamak Monitoring System (TMS) in KSTAR consists of nearly eight GUI **layouts**. ... operation methods in Table 1 so that all PVs in a single GUI **layout** can be ... Client side security has been implemented in code base without using a security **description file**. ...

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[CITATION] An Adaptive Architecture for Multi-stream Authoring and Presentations in Distributed Networks
CM Huang, PC Liu, TH Hsu

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[Integrated circuit analysis and design involving defective circuit element replacement on a netlist](#)

F Hayem, P Arnold - US Patent 6,546,514, 2003 - Google Patents

... features, validates the design, places components onto a chip **layout** and routes connections between components. ... The behavior **description file** is design (CAD) system, in accordance with an embodiment of written in a hardware **description language** (HDL), such as the ...

[Related articles](#)

[System and method of defining the security condition of a computer system](#)

GS Gales, CD Anderson, RL Schertz, RP ... - US Patent App. 10/ ..., 2001 - Google Patents

... [0007] In yet another embodiment of the present invention, a system of defining security **conditions** of a computer system comprises a vulnerability **description file** containing a definition of at least one attack and a definition of at least one policy item for the attack. ...

[First International Symposium on Cyber Worlds \(CW'02\)](#)

CM Huang, PC Liu, TH Hsu - computer.org

... The QoDML generator is capable of transforming the provisional information into the **description file** represented by ... Table 1. Three types of elements that are contained in QoDML are **layout**, media object ... For the **layout** type, the region element defines a block of space in the ...

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[PDF] [BEHAVIORAL ROBOT SIMULATION FOR PRODUCTION SYSTEMS](#)

[uni-miskolc.hu \[PDF\]](#)

T JUHÁSZ - ait.iit.uni-miskolc.hu

... up life-cycle assessments by virtual prototypes allow devising the optimal **layout** and the best ... and the X3D environment that is a standard 3D scene **description language** introduced by ... in a tree structure that are handled and stored hierarchically in the X3D scene **description file**. ...

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[Technology-independent design using the ASA silicon compiler](#)

CWH Strolenberg - Microelectronics Journal, 1992 - Elsevier

... New processes are characterised in ASA by creating an EDIF process **description file** containing the design rules. ... not differ from any other library in that it contains cells with related **layout files**. The **layouts** of the cells are specified in so-called Symbolic CIF, which means that the ...

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[The Design and Development of an Adaptive Web-Based Learning System](#)

C Wang - Human Interface and the Management of Information. ..., 2009 - Springer

... In this subsection, we introduce an XML-based content **description language**, called Adaptive Multimedia Markup Language ... 3, an AMML **description file** is consisted of three major blocks. ... block describes a presentation's properties of the spatial dimension, ie, the **layout** of the ...

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[\(PDF\) Hierarchy Content Organization and Delivery for Mobile Engineering Computing](#)

[psu.edu \(PDF\)](#)

X Su, BS Prabhu, CC Chu, R Gadh - CiteSeer

... Fig.3. An instance of sub-division sequence and **layout** ... etc, will be used to form the root and it will constitute the top level **description file**. ... The system has performed satisfactorily in representing different engineering graphics content (drawings, schematics, **layouts**), which is quite ...

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[A Formal Specification of Interaction Widgets Hierarchy Framework](#)

LJ Wang, ASM Sajeev, L Inchaiwong - 2006 - computer.org

... of adaptable application interface, task-model based approach and XML style interface **description language**. ... bounds AIOWindow to JavaWindow or WinNTWindow in its presentation **description file**, and [2 ... There are three newly added properties here, State, Mode, and **Layout**. ...

[Related articles](#) - [All 4 versions](#)

[Automatic generation of programmable logic device architectures](#)

V Belz, J Rose - US Patent 7,051,313, 2006 - Google Patents

... 5 is an example architecture **description file**; allow PLD designers to write software code (in either C ... FIG 12 is a simplified block diagram of a tiled **layout** Accordingly, there is a ... performance evaluation of a wide With a high-level PLD architecture **description language**, variety of ...

[Cited by 3](#) - [Related articles](#) - [All 2 versions](#)

[Representation and Visualization of Designs with the Use of Hierarchical Graphs](#)

P Nikodem - Computer Vision and Graphics, 2006 - Springer

... Although GML is a powerful **description language** for graph drawing purposes and includes an ex ... Visualization **description file** will be dynamically connected to classes of objects or to ... Searching for innovative structural **layouts** by means of graph grammars and evolutionary ...

[Related articles](#) - [BL Direct](#)

[\(PDF\) A Hierarchical Description Language and Packing Algorithm for Heterogenous FPGAs](#)

[toronto.edu \(PDF\)](#)

J Luu - 2010 - eecg.toronto.edu

... 28 iv Page 5. 3 Complex Block Architecture **Description Language** 32 3.1 Introduction
reduced from 35 to 18 in designs with hard blocks [24]. This is under the **condition** that the number of hard blocks supplied on the FPGA exactly matches the demand of the user circuit. ...

[Related articles](#) - [View as HTML](#) - [All 2 versions](#)

[Logic diagram display method, program, and apparatus](#)

E Furukawa - US Patent 7,634,750, 2009 - Google Patents

... Sun J Lin (74) Attorney, Agent, or Firm—Staas & Halsey LLP (57) ABSTRACT In cell retrieval

processing, a cell list is acquired from a library, a classification file corresponding to the cell list is read to display a classification screen of setting a cell retrieval **condition**, and a cell ...

[All 2 versions](#)

Automatic logic design supporting method and apparatus

K Matsumoto, K Adachi, T Ishida - US Patent App. 09/811,402, 2001 - Google Patents
... 1 111 113 RTL **DESCRIPTION FILE** LOGIC CIRCUIT **DESCRIPTION FILE** 115 LOGIC CIRCUIT
DESCRIPTION FILE 117- **LAYOUT DATA FILE** ... process for giving a wire length budget to a net
in the logic circuit to **lay out** in such a manner that the **layout** process becomes ...

SIRENA: A CAD environment for behavioural modelling and simulation of VLSI cellular neural network chips

[psu.edu](#) [PDF]

R Carmona, I García-Vargas, G Liñán, ... - ... Journal of Circuit ..., 1999 - interscience.wiley.com
... It employs a dedicated high-level hardware **description language**: DECEL, fitted to the description
of non ... class of translation-invariant CNNs, where all inner cells are identical, **layout** is very ... A
network **description file**, written in DECEL, is fed into GMS and the proper linkage to ...
Cited by 5 - Related articles - BL Direct - All 10 versions

An integrated approach to rapid product development for embedded automotive control systems

U Lefarh, U Baum, T Beck, K Werther, T ... - Control Engineering ..., 1998 - Elsevier
... performed by separate teams: 1. Function development: control-system **layout**, some- times ... ECU
with integ- rated ERDOS operating system, but also an ASAP2 **description file**. ... editor, the model
can be specified textually in ESDL (Embedded Software **Description Language**). ...
Cited by 3 - Related articles - BL Direct - All 2 versions

[PS] Untyped MIG: What Has Changed and Migration Guide

[opengroup.org](#) [PS]

F Travostino - opengroup.org
... 12 - 4. Migration Guide To migrate a MIG **Description File** from MACH3 MIG to Untyped MIG, few
rules need to be followed. ... In fact, this operation requires an deep knowledge of the **layout** of a
Reply message. ... The MIG **Description Language** file is: subsystem test 1200; ...
Related articles - View as HTML - All 6 versions

[PDF] Project Specification and Plan

[st-andrews.ac.uk](#) [PDF]

A Macdonald - panda.cs.st-andrews.ac.uk
... services client – they simply give the system a link to the services **description file**. ... SOAP SOAP
(Box, et al., 2000) is a message **layout** specification that describes a ... annotation efforts have
focused on describing web services using the Resource **Description Language** (RDF), and ...
Related articles - View as HTML - All 2 versions

User interface management systems

J Ero, R van Liere - Advances in Computer Graphics III, 1988 - books.google.com
... the architecture of the UIMS: • The syntax and semantics of the UI **description language**. ... 6)) displays
colors input devices voice input and output screen **layout** menus • interaction ([7 ... of workstation,
extra table height of keyboard, adjustable monitor lighting **conditions** noise level ...
Cited by 3 - Related articles - All 3 versions

[\[PDF\] A Closer Investigation of Service Discovery Protocols in Multihop Mobile Ad Hoc Networks](#)

[uniroma1.it \[PDF\]](#)

AN Mian, R Beraldi, R Baldoni - IEEE Pervasive Computing - dis.uniroma1.it

... The **description file** is a plain text file that has all the information about the service. ... SDPs can also be independent of any **description language**. ... Broadly speaking architecture specifies the **layout** of any structure and how major components of that structure are connected with ...

[Cited by 1](#) - [Related articles](#) - [View as HTML](#)

[\[PDF\] Betz et al.](#)

[toronto.edu \[PDF\]](#)

PL DEVICE - eecg.toronto.edu

... 26 Claims, 12 Drawing Sheets ARCHITECTURE DESCRIPTION FILE CHECK FOR FUNCTIONAL AND SEMANTIC ERRORS REPLICATE VARIANTS OF THE BASIC ELEMENTS & STITCH TOGETHER ARCHITECTURE GENERATION ENGINE EVALUATE OVERALL ...

[Related articles](#)

[Seamless integration of interactive forms into the Web](#)

[psu.edu \[PDF\]](#)

A Girsensohn, A Lee - Computer Networks and ISDN Systems, 1997 - Elsevier

... is not only used for creating a window but also for changing **layouts** that become ... The alternative chosen was a preprocessor that converts a field **description file** to Java source ... Dynamic Forms supports the Java **layout** mechanism so that other Java interface components can be ...

[Cited by 5](#) - [Related articles](#) - [All 9 versions](#)

[KBSIM: a system for interactive knowledge-based simulation](#)

M Hakman, T Groth - Computer methods and programs in biomedicine, 1991 - Elsevier

... complicated programming for Unix shared memory, queues and semaphores, etc., a simple table **layout** was communicated ... 2.4.1. Rule **Description Language** The Rule **Description Language** is based on the production rule model. ... 9. The rule **description file** has three sections. ...

[Cited by 8](#) - [Related articles](#) - [All 4 versions](#)

[\[CITATION\] Computer-aided design of fuzzy systems based on generic vhdl specifications](#)

IT Fuzzy - IEEE Transactions on Fuzzy Systems, 1996

[Cited by 2](#) - [Related articles](#)

[\[PDF\] Document analysis components](#)

[psu.edu \[PDF\]](#)

A Anjewierden - IBROW Deliverable D2b (part 2) - Citeseer

... updated version of the library now contains documentation of the Document **Lay-out** Analysis components ... The short term objective is to make the document **layout** discovery functionality of ... Input specification **Description file**(File) A local File kb(KB) A local knowledge base KB url ...

[Cited by 2](#) - [Related articles](#) - [View as HTML](#) - [All 6 versions](#)

[Automatic hypertext conversion of paper document collections](#)

[psu.edu \[PDF\]](#)

A Myka, U Guntzer - Digital Libraries Current Issues, 1995 - Springer

... The developed link type **description language** LTHP (Link Type **description language** for HyperText ... as detected during preprocessing, are taken from a structure element **description file** that can be ... Within paper documents however, an author may have used **layout** properties in ...

[Cited by 11](#) - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

A new concept of a unified parameter management, experiment control, and data analysis in fMRI: Application to real-time fMRI at 3 T and 7 T

M Hollmann, T Mönch, S Mulla-Osman, C ... - Journal of neuroscience ..., 2008 - Elsevier
... this repository in terms of a newly developed XML-based **Experimental Description Language** (EDL). ... As another advantageous feature the editor **layout** is constructed semi-automatically by reading the ... measurement consisted of a block of 42 images for each **condition**: 3 times ...

[Cited by 2](#) - [Related articles](#) - [All 4 versions](#)

[PDF] Update on the Development and Validation of MERCURY: A Modern, Monte Carlo Particle Transport Code

[lnl.gov](#) [PDF]

RJ Proccassini, JM Taylor, MS McKinley, ... - Proceedings of the ..., 2005 - nuclear.lnl.gov
... Figure 1. The **layout** of the fusion-neutron shielding test facility. ... A search for a new parser was begun, which led to the use of the XML data-**description language** and the ... processing), this new approach allows one to add a new section to the data-model **description file** for each ...

[Cited by 2](#) - [Related articles](#) - [View as HTML](#) - [All 4 versions](#)

Apparatus and method for controlling user interface

H Kawamoto, M Aritomi, T Minagawa - US Patent 7,617,247, 2009 - Google Patents
... US Patent US 7,617,247 B2 Nov. 10, 2009 Sheet 3 of 17 FIG. 3 CONFLICT PROCESS RULE
DESCRIPTION FILE 305 INTERNALSTRUCTURE R/W Page 6. ... (1) Collate(OFF) < Staple(ON).
(2) Group(OFF) < **Layout**(BOOKLET). (3) Collate(ON) < true. (4) Group(ON) < true. ...

[All 2 versions](#)

IMISSED: Interactive microprocessor instruction set simulation environment with graphical display

E Dirks - Microprocessing and Microprogramming, 1989 - Elsevier
... A standard programming language is used as a hardware **description language** and the system consists of a ... Colours and a clear screen **layout** have been used to enhance the bandwidth from ...
The simulation system inputs these code files and/or state **description file**, which is in ...

[All 2 versions](#)

[PDF] Hardening FPGA-based systems against SEUs: A new design methodology

[psu.edu](#) [PDF]

L Sterpone, M Violante - Journal of Computers, 2006 - CiteSeer
... starting either from a schematic, or a behavioral model coded in a hardware **description language**. ...
obtained routing graph is colored according to the information contained in the **Layout Description file**. ... Indeed, when this **condition** is met we have that the SEU effects propagated ...

[Cited by 1](#) - [Related articles](#) - [View as HTML](#) - [All 12 versions](#)

An experimental representation for organizational level designs and synthesis

JR Bammi - Proceedings of the 1985 ACM thirteenth annual ..., 1985 - portal.acm.org
... capture, synthesis, simulation and documentation, in a RT (Register Transfer) level hardware **description language**. ... mapping to the final implementation by the next synthesis step to physical **layout**. ... to be declared at one place, instead of being scattered in the **description file**. ...

[Related articles](#) - [All 2 versions](#)

[HTML] Sharing Functionality on the Web

[gwdg.de \[HTML\]](#)

T van Veen, G Petz, C Sadilek, M ... - D-Lib Magazine, 2009 - webdoc.sub.gwdg.de

... However, TEL cannot be held responsible for services that users put in their own service

description file. ... In those cases, the data or service provider may offer a very simple service without complicated **layout** for this specific ... [WSDL] Web Services **Description Language**: 2.0: <http ...

[Related articles](#) - [Cached](#) - [All 3 versions](#)

[PDF] TOPS: A Target-Oriented Partial Scan Design Package Based on Simulated Annealing

[hindawi.com \[PDF\]](#)

CP Ravikumar, H Rasheed - VLSI Design, 1994 - downloads.hindawi.com

... The new configuration is accepted under two **conditions**. If both the profit and cost parameters of the new configuration are better, then S is accepted. ... The computation of the cost of a configuration de- pends necessarily on the implementation technology and the **layout** style. ...

[View as HTML](#) - [All 5 versions](#)

Image supply apparatus, control method therefor, and printing system

F Goto, H Hirabayashi, T Aichi, A ... - US Patent App. 11/ ..., 2005 - Google Patents

... environment can be constructed, the DSC can grasp the printer capacity, and print **conditions**

can be ... items (eg, the size and type of available print medium, print quality, and **layout**) in the ... [0022]

DPOF has been described, and this also applies to a **description language** such as ...

[All 4 versions](#)

[PDF] Workflow Management Coalition Workflow Standard Process Definition Interface-XML Process Definition Language

[psu.edu \[PDF\]](#)

XMLPD Language - 2005 - Citeseer

... Xpression element in **Condition** element. ... 1. Pools (and their lanes) are associated with processes and are used in **layout** and to define ... 2. Message flows are used to represent communication between processes, based on Web Services **Description Language** (WSDL) protocols. ...

[View as HTML](#) - [All 19 versions](#)

Program comprehension for Web services

N Gold, K Bennett - 2004 - computer.org

... Web Services **Description Language** (WSDL) allows the description of a web service so that a ...

When data service providers publish the DAS **description file** into the registry, they publish ... these issues on existing approaches, identify some new requirements and **lay out** areas for ...

[Cited by 7](#) - [Related articles](#) - [All 6 versions](#)

Behaviour-based multiplayer collaborative interaction management

Q Lin, W Wang, L Zhang, JM Ng, ... - ... Animation and Virtual ..., 2006 - interscience.wiley.com

... **KEY WORDS**: collaborative virtual environment; multiplayer role-playing; collaborative interaction management; Collaborative Behaviour **Description Language** ... interaction management strategy is that it considers the spatial distance as the dependent **condition** for enabling the ...

[Related articles](#) - [BL Direct](#) - [All 3 versions](#)

Experiences of teaching semicustom design and testing

EG Chester - Microprocessors and Microsystems, 1990 - Elsevier

... Training in these hardware **description language**. ... Many students fall into bad habits when designing The circuit **description file** (including placement digital systems ... a logic incorporated a synchronous counter to sequence oper- system on a PCB using CAD **layout** and simulation ...

[All 4 versions](#)

(PDF) An information model for finding and integrating distributed resources for engineering design-manufacturing processes [yonsei.ac.kr](#) (PDF)

R Kulkarni, D Rosen, JK Allen, F ... - Proceedings of the ..., 2002 - cadcam.yonsei.ac.kr

... The web service **description language** (WSDL) [6] is an XML based format accepted internationally for ... Figure 4: Information Model **Layout** for Design and Manufacturing ... and corresponding living systems theory function keywords are stored in the resource **description file**. ...

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